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PTO/SB/97 (08-00)

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In re: application of: Mihail lotov
Application Number: 10/655,695
Filed: September 5, 2003
Confirmation Number: 7181

Title: Techniques for Graphical Analysis and Manipulation of Circuit Timing Requirements

Atty Docket Number: 015114-066000US SJC/djb

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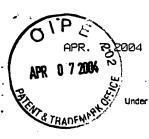
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TRANSMITTAL FORM			Application Number		10/655,695	
			Filing Date		September 5, 2003	
			First Named Inventor		lotov, Mihail	
(to be used for all correspondence after initial filing)			Art Unit 2671			
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Applicant/Patent Owner: Mihall lotov									
Application No./Patent No.: 10/655,695 Filed/Issue Date: September 5, 2003									
Entitled: Techniques for Graphical Analysis and Manipulation of Circuit Timing Requirements									
Altera Corporation a Corporation									
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